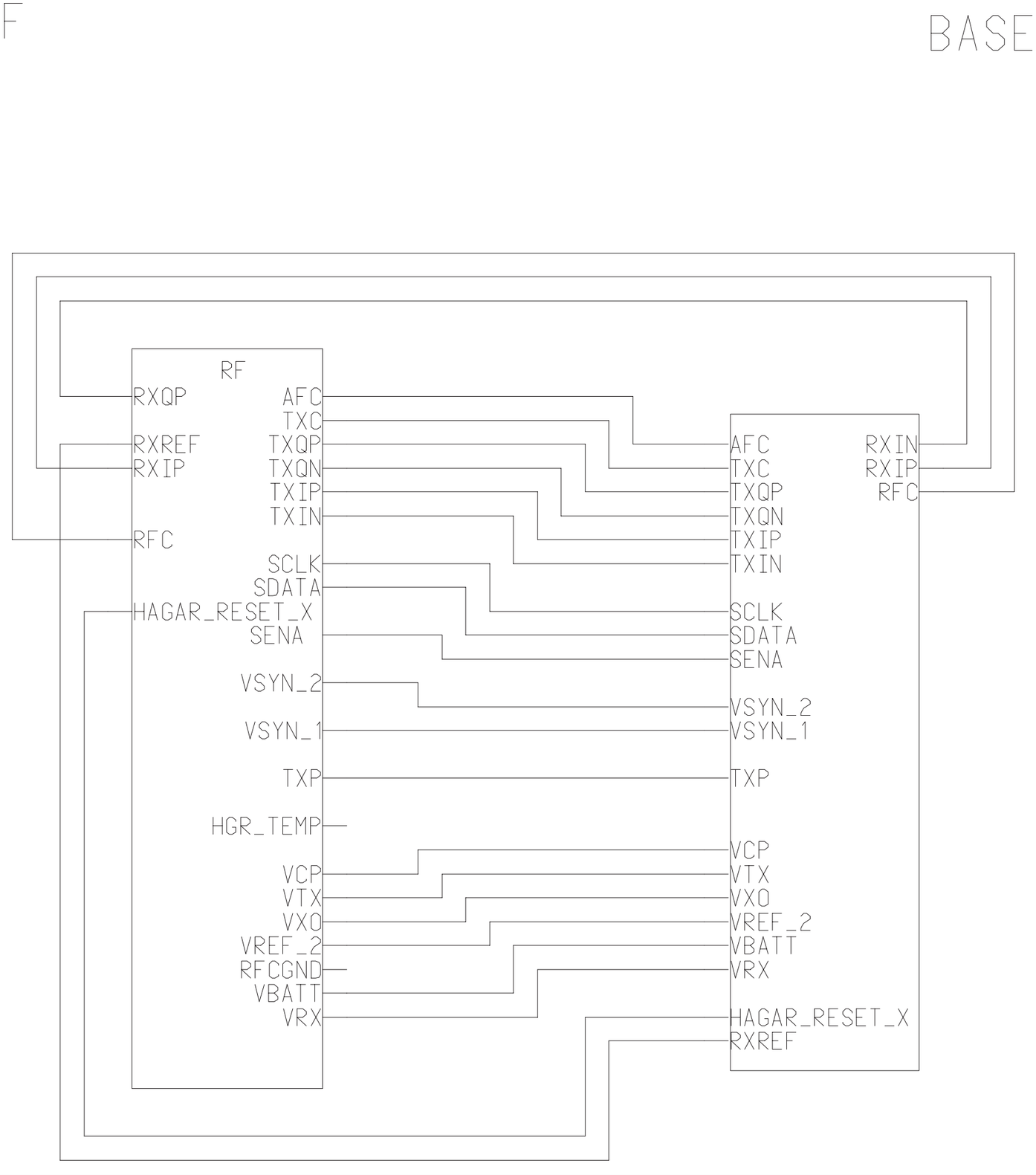
Connection between RF and Baseband modules (HW2103 Edit 2) for layout version 21



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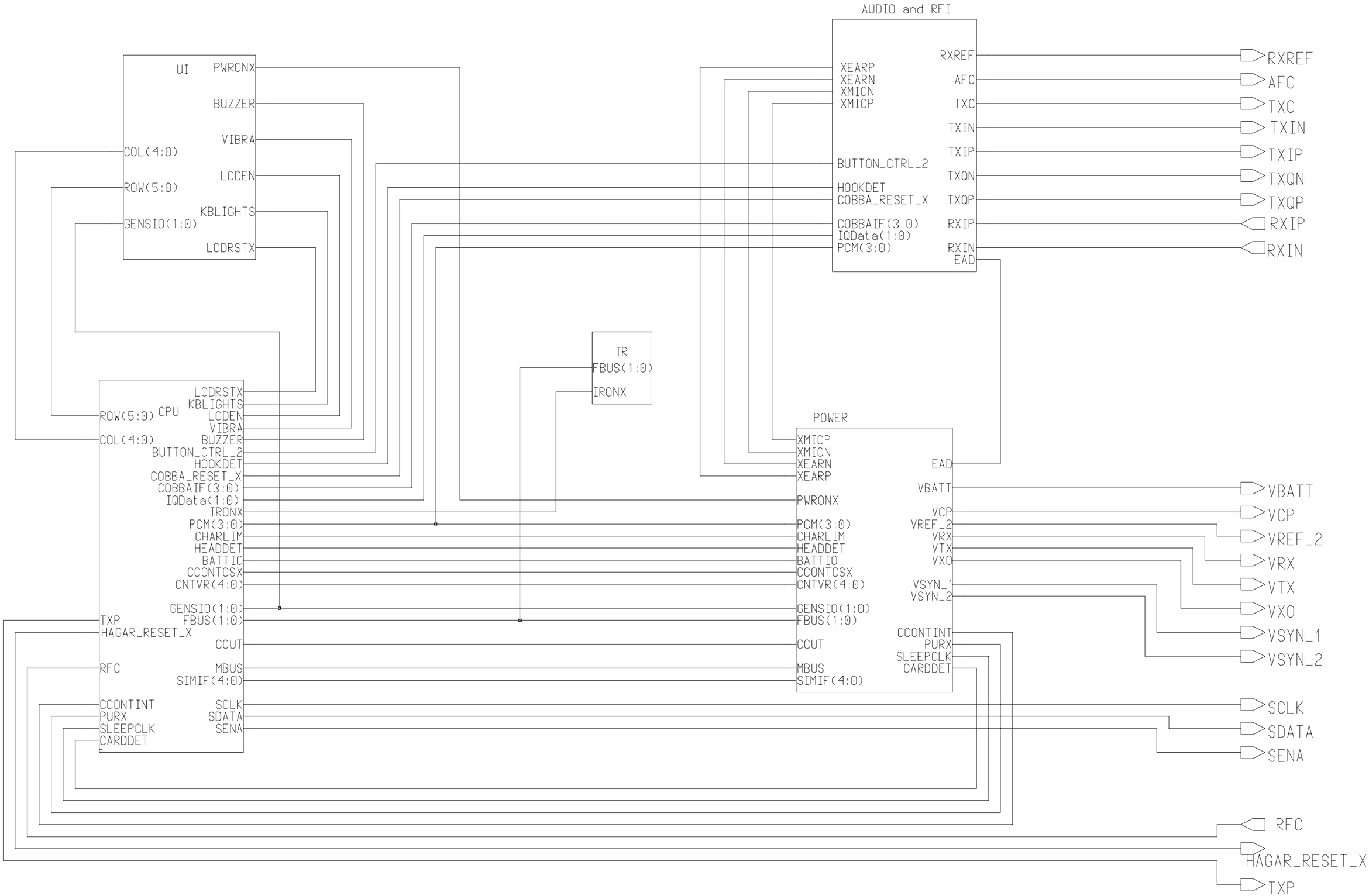
Baseband Block Interconnections (HW2103 Edit 3) for layout version 21



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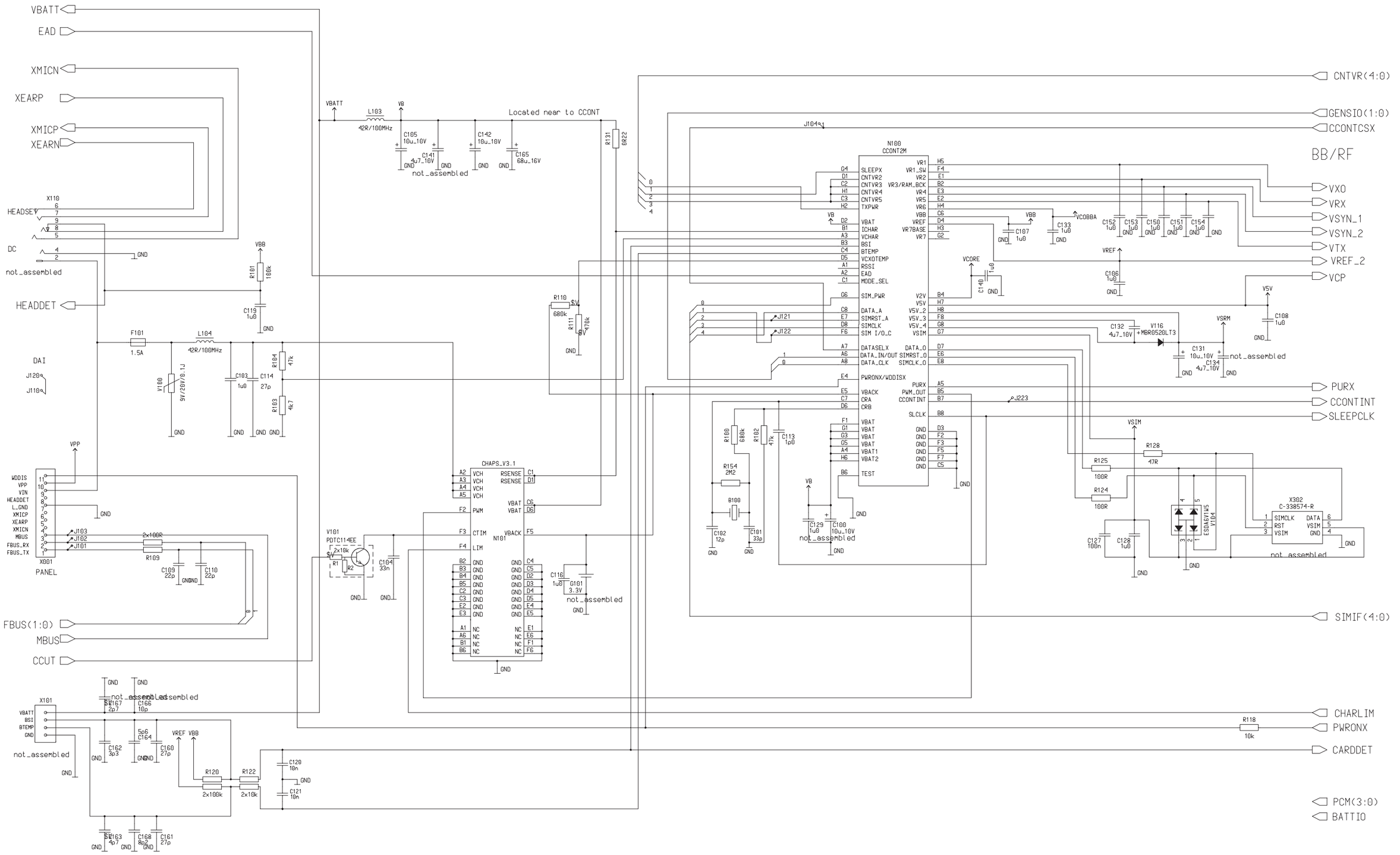
Circuit Diagram of Power Supply (HW2103 Edit 5) for layout 21



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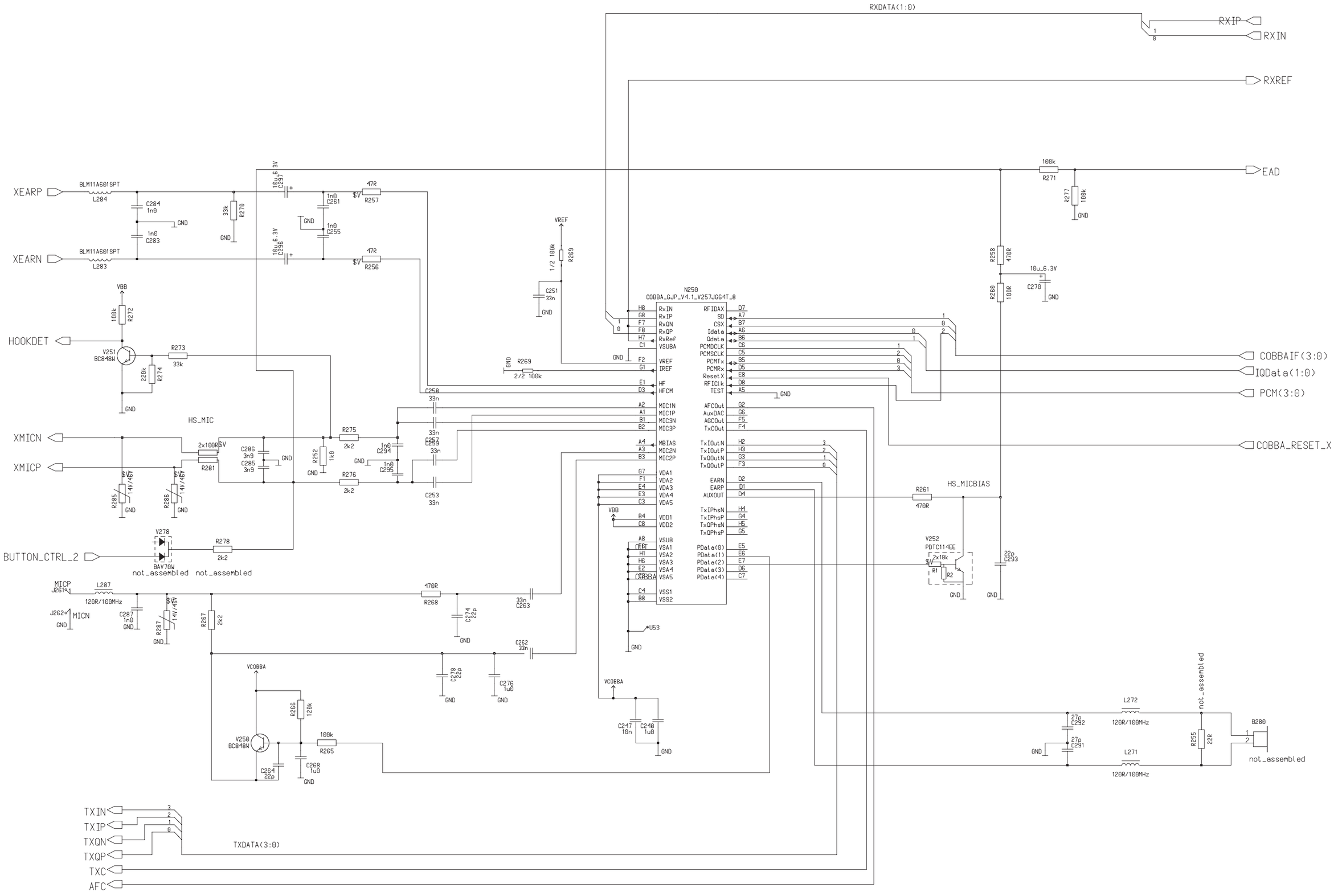
Circuit Diagram of CPU Block (HW2103 Edit 5) for layout version 21



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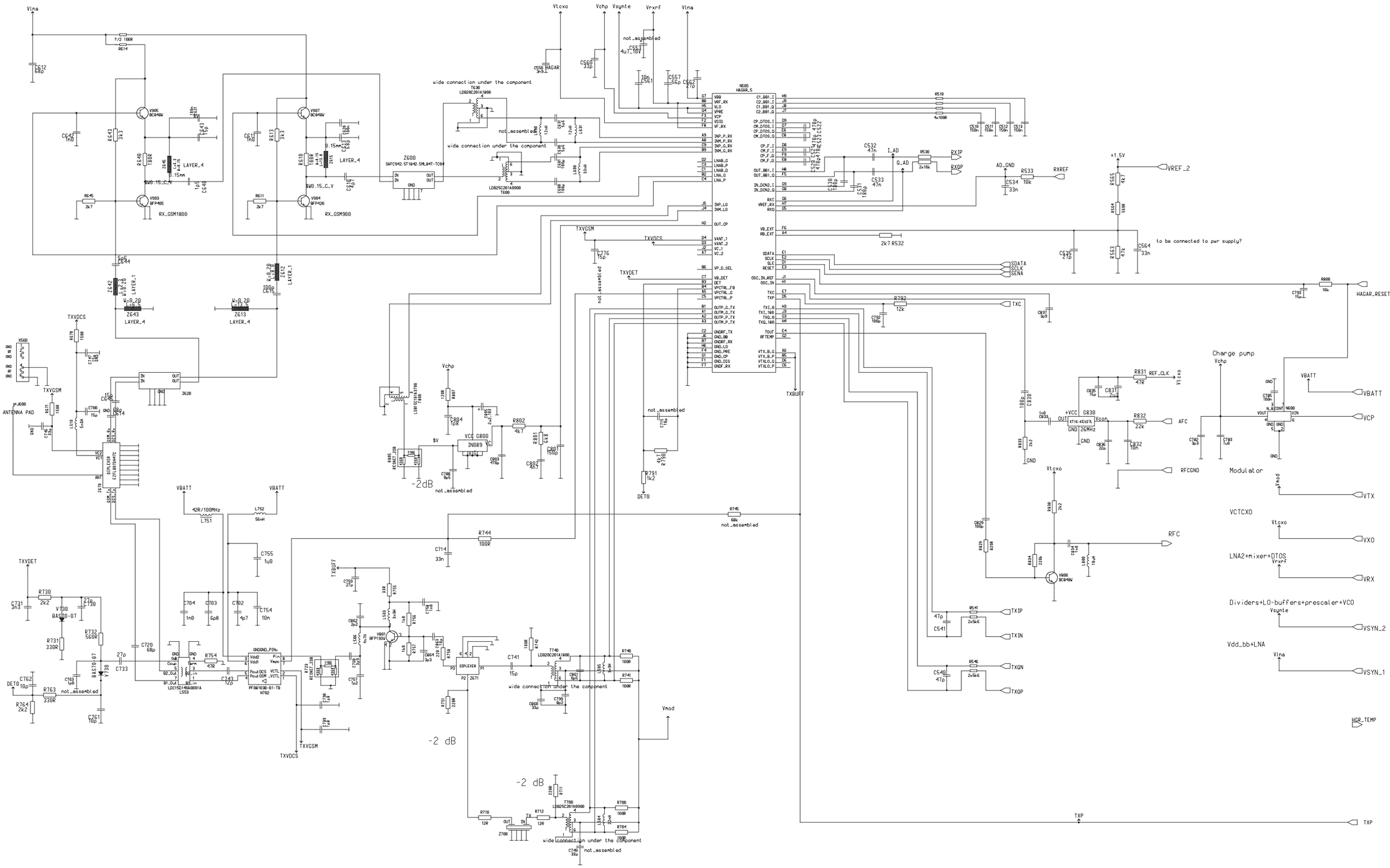
Circuit Diagram of RF Block (HW2103 Edit 5) for layout version 21



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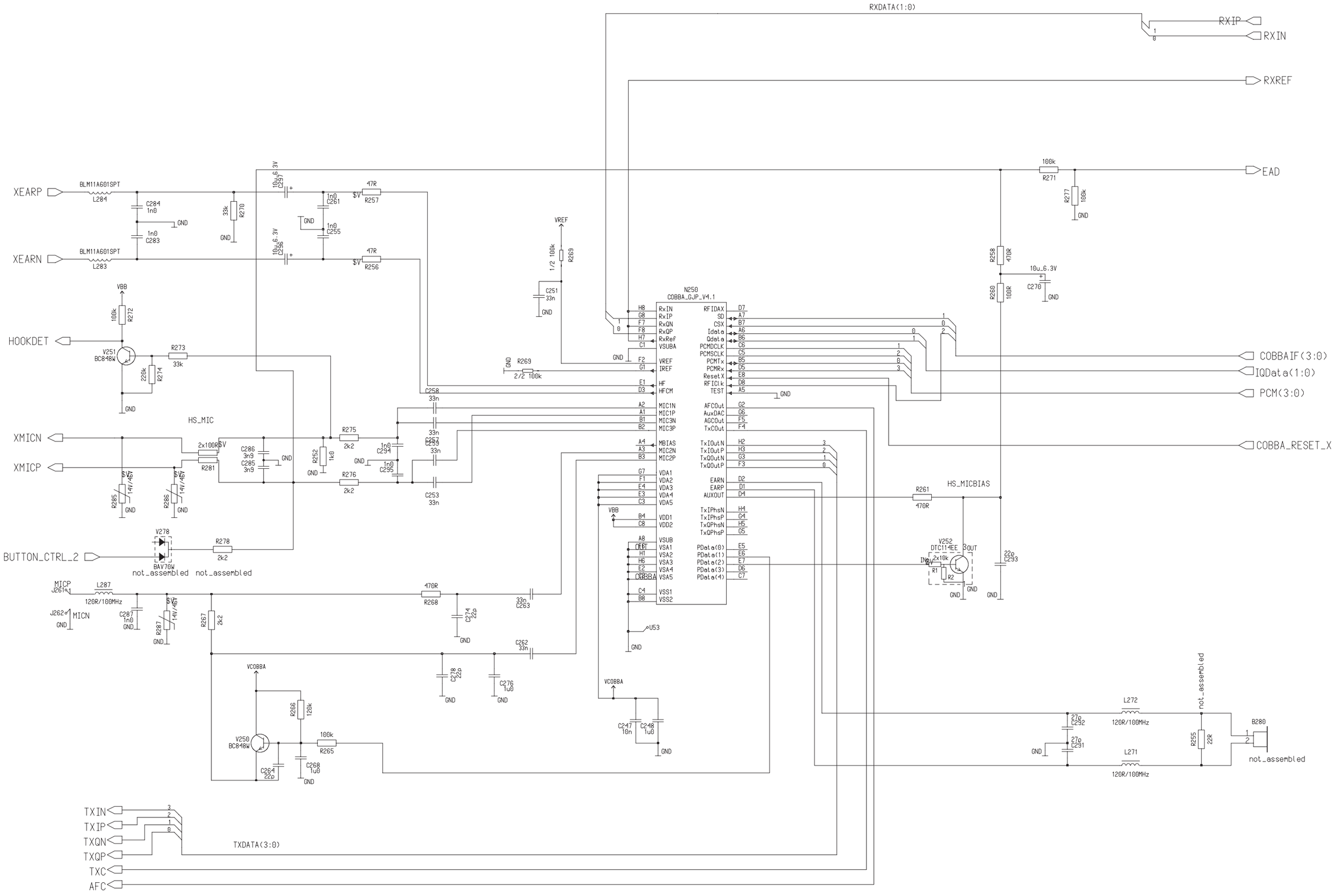
Circuit Diagram of Audio and RFI (HW2103 Edit 20) for layout version 21



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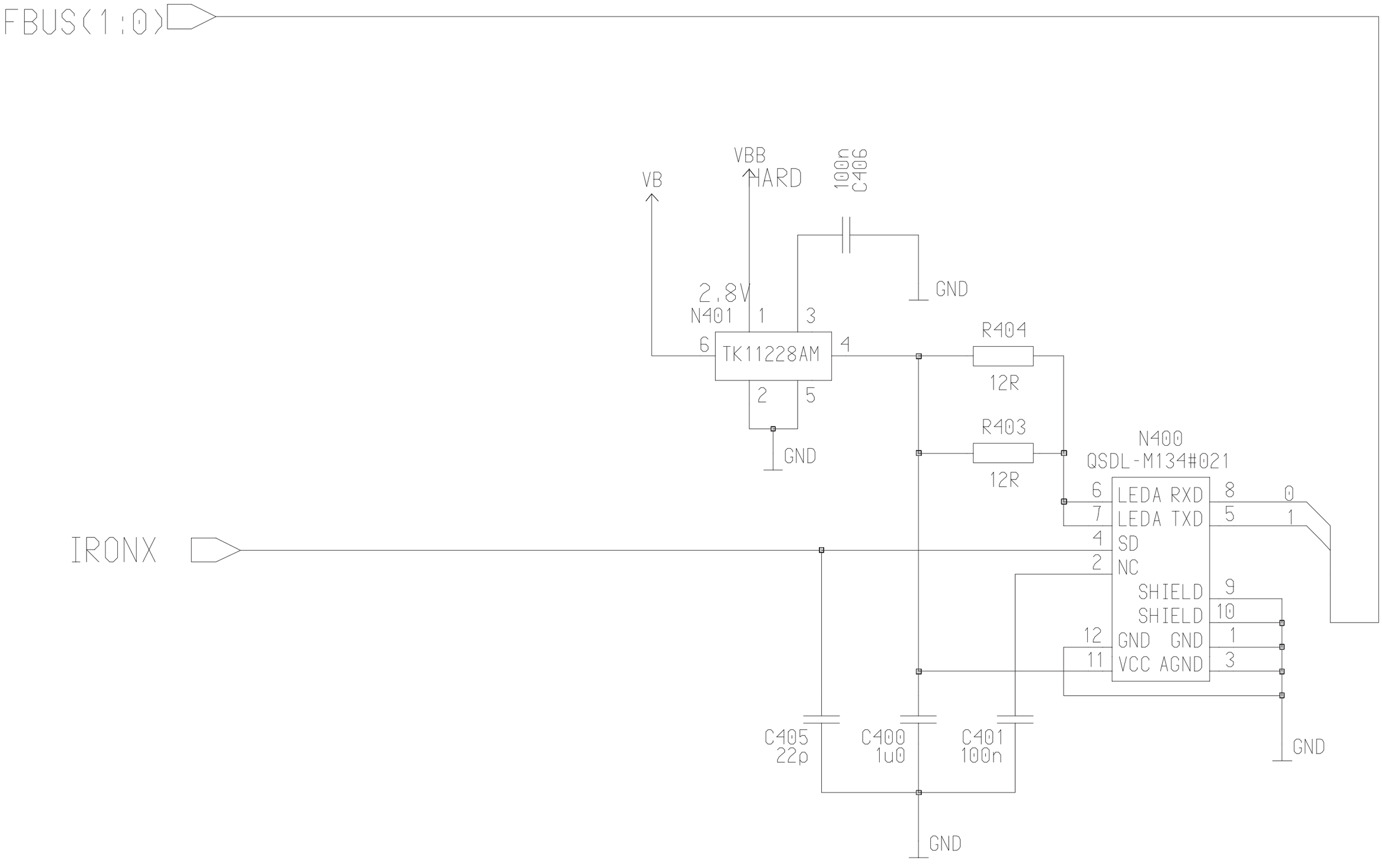
Circuit Diagram of IR Module (HW2103 Edit 17) for layout version 21



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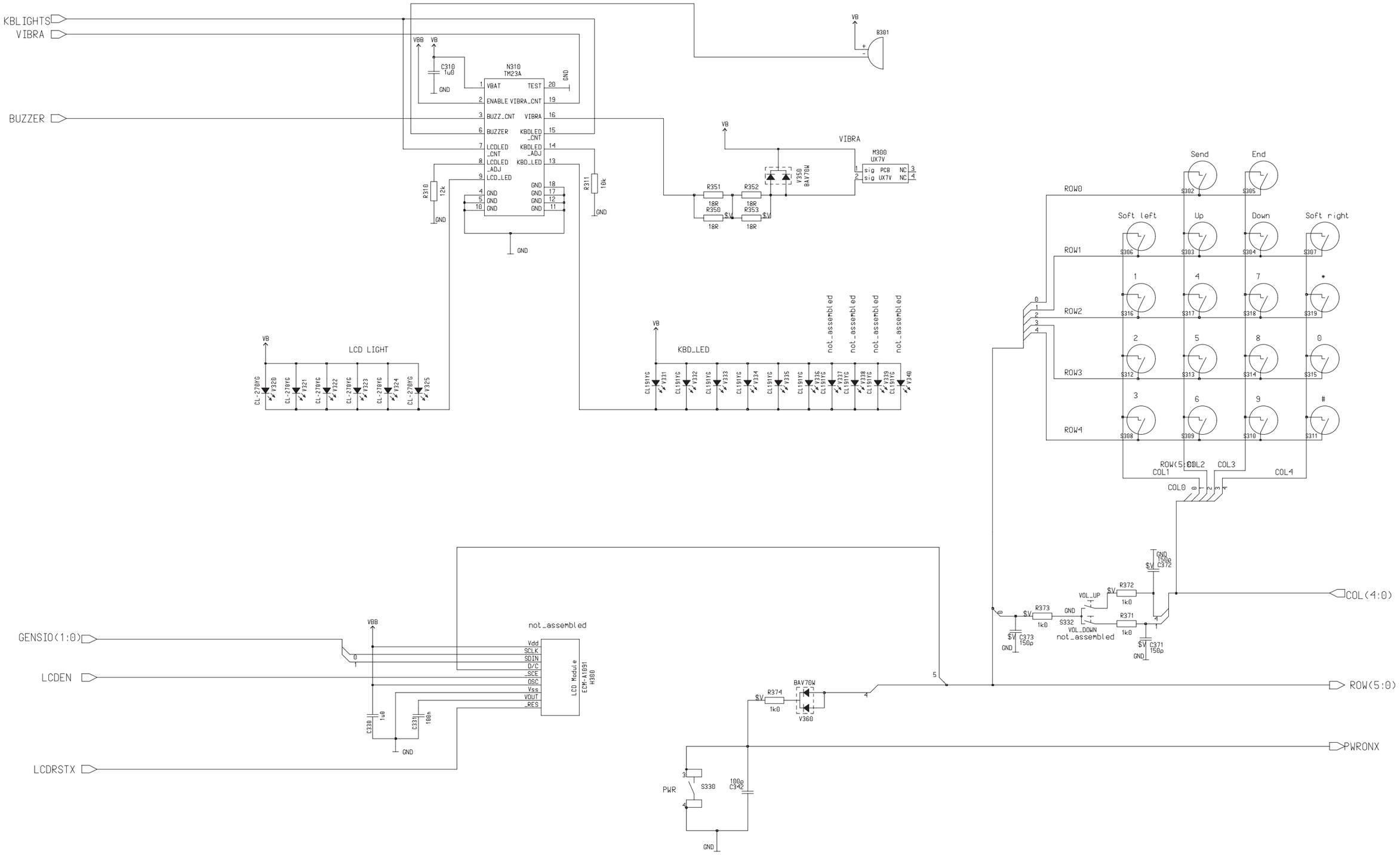
Circuit Diagram of UI (HW2103 Edit 3) for layout version 21



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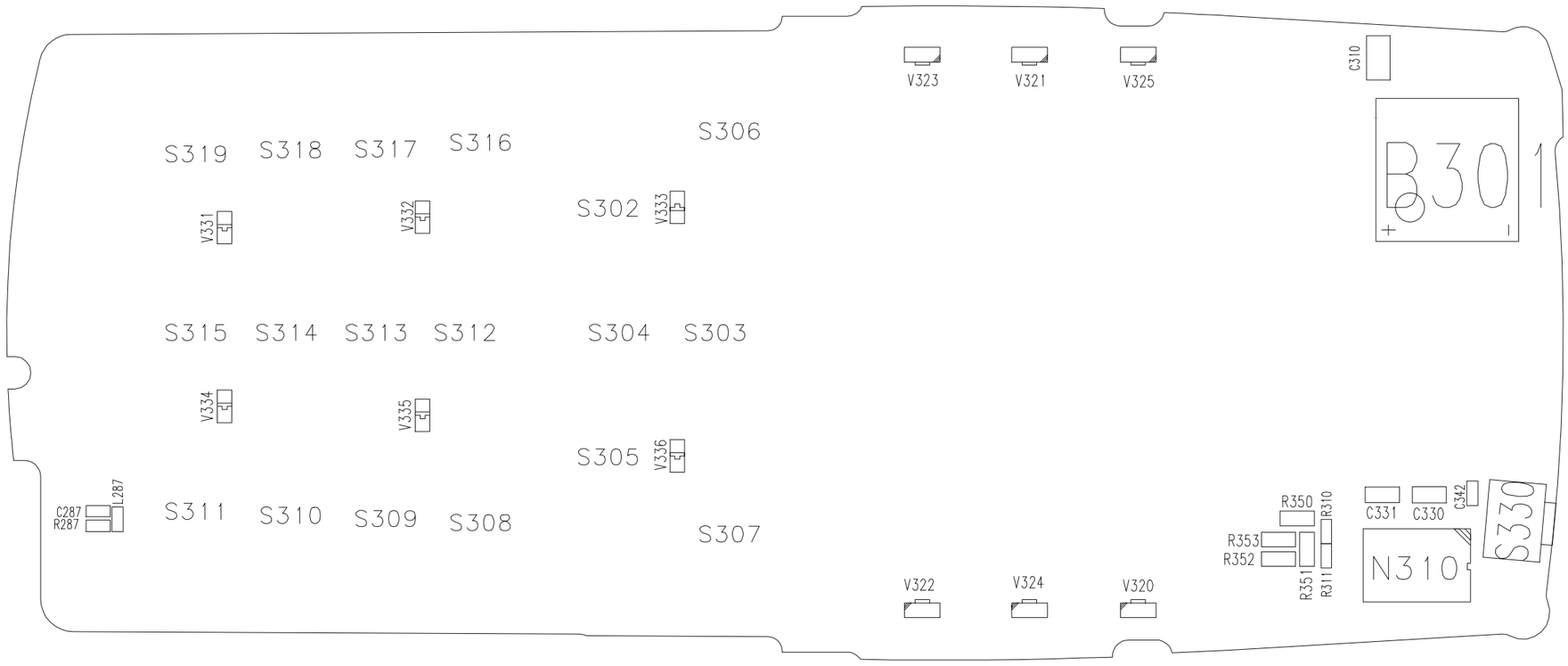
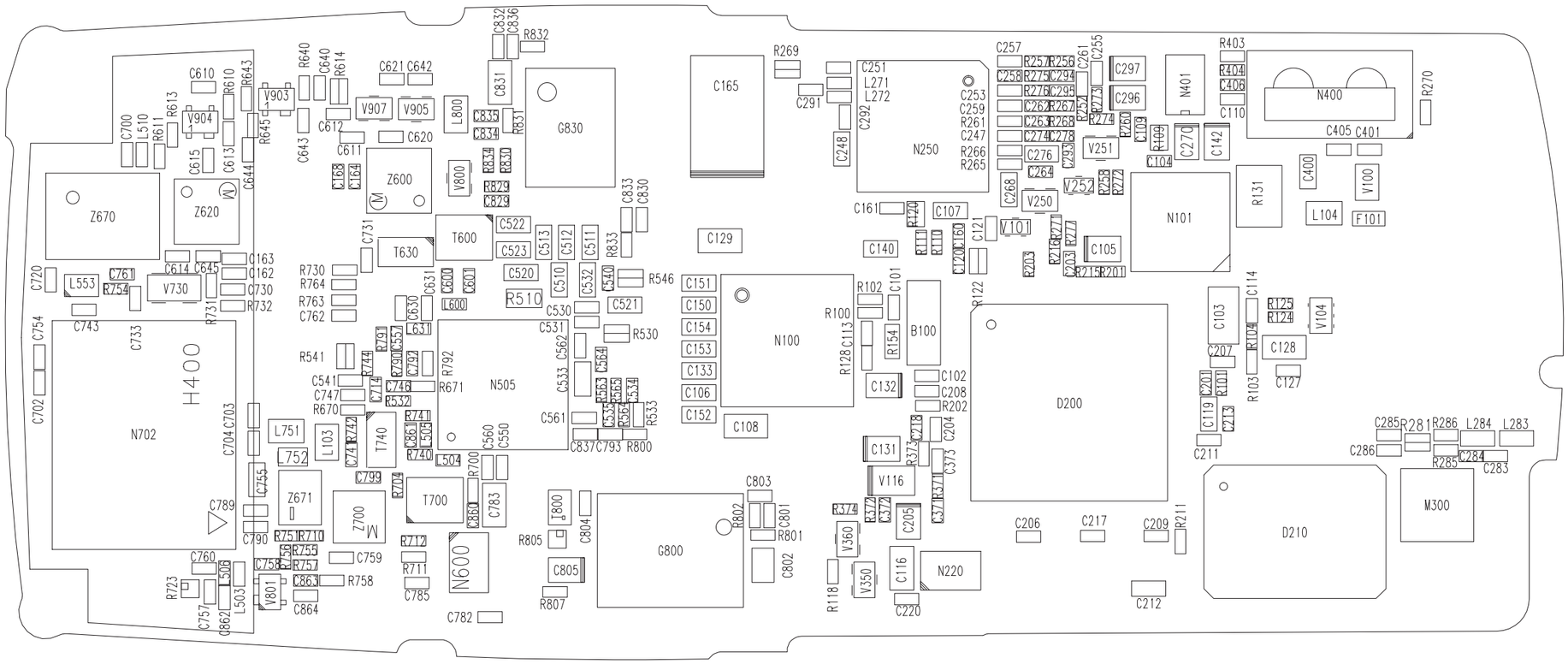
Layout Diagram of RM7L – Top (Version 21)



Layout Diagram of RM7L – Bottom (Version 21)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N702
Z670
Z620
Z600
T630
T600
G830
C165
N250
N401
N400
N101
R131
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F101
V100
V730
L553
V903
V904
V907
V905
V800
L800
G800
T740
T700
Z700
Z671
N600
N505
N100
B100
D200
D210
M300
N220
V251
V252
V250
V101
V104
L751
L752
L103
L504
L503
L506
V801
V360
V350
V116
C803
C801
C802
R801
R802
C805
R807
C782
C785
C864
C862
C757
C760
C789
C790
C755
C704
C703
C702
C754
C720
C743
C733
C761
R754
R731
C614
C645
C163
C162
C730
C732
R730
R764
R763
R762
C631
C630
C600
L600
C601
R510
C520
C522
C523
C513
C512
C511
C510
C532
C540
R546
C521
C530
C531
R530
C562
C533
C564
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C534
C561
C837
C793
R800
R533
C536
C535
C560
C550
R700
C783
C860
R712
R711
R723
R758
C759
C758
R751
R710
R756
R755
R757
C863
C611
C612
C620
C621
C642
C610
R610
R643
R645
R640
C640
R614
C643
C644
C615
C613
R613
R611
C700
L510
C168
C164
C731
C832
C836
R832
C831
C835
C834
R831
R829
C829
C833
C830
R833
C129
C151
C150
C154
C153
C133
C106
C152
C108
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C541
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R670
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R671
R532
R741
C861
C505
C740
C741
C742
C744
C745
C743
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C792
R744
R792
C774
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R742
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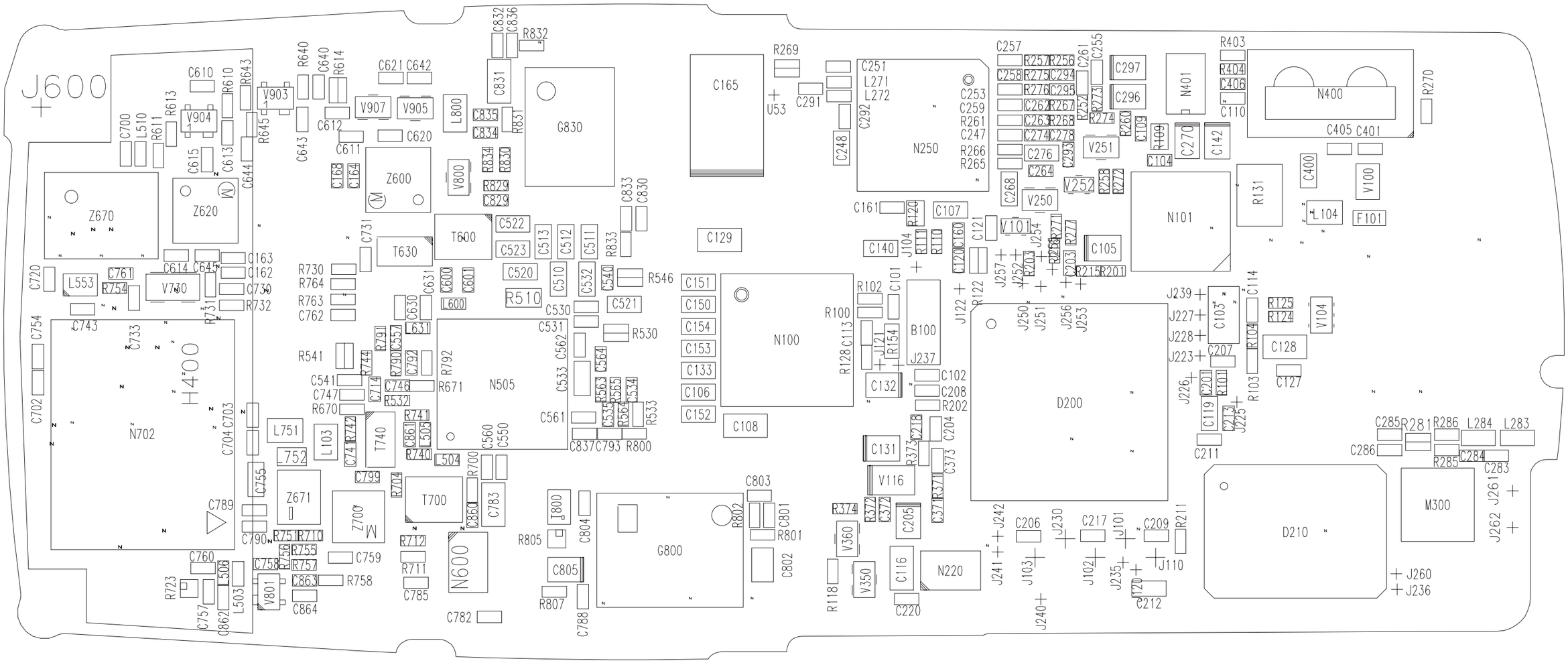
C746
C744
C745
C774

Testpoints of RM7L (Version 21)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C834
R831
G830
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R269
U53
C291
C248
C251
L271
L272
C292
N250
C253
C259
R261
C247
R266
R265
C257
R257
R256
C258
R275
C294
C261
C255
C262
R267
C263
R268
C274
C278
C276
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R252
R273
R274
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V101
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R201
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C152
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R102
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R128
C113
C101
B100
J237
J121
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C132
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R202
C108
C131
C218
C204
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